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PATENT & TRADEMARK OFFICE

GP-2811

#8/Election
3/20/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:	10/059,898	Confirmation No.:	8562
First Named Inventor:	Siniaguine, Oleg	Filing Date:	January 28, 2002
Group Art Unit:	2811	Examiner:	Vu, Hung K.
Atty. Docket No.:	M-4532-8P US		
Title:	Integrated Circuits And Methods For Their Fabrication		
Assignee:	Tru-Si Technologies, Inc.		

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San Jose, California
December 4, 2002

COMMISSIONER FOR PATENTS
Washington, D. C. 20231

REPLY TO RESTRICTION REQUIREMENT

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Dear Sir:

The Office Action dated November 5, 2002 requires a restriction to one of the following groups of claims:

I. Claims 1-10; or
II. Claims 11-27.

Applicants elect Claims 1-10.

The undersigned can be contacted by telephone at the number below with any questions regarding this case.

EXPRESS MAIL LABEL NO:

EV 192 675 724 US

Respectfully submitted,

Michael Shenker

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